

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Leonard Forbes

Title:

IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY

INSULATORS

Docket No.:

1303.027US1

Serial No.: 09/945,512

Filed: Examiner: August 30, 2001 Richard A. Booth Due Date: N/A

Group Art Unit: 2812

Commissioner for Patents

Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

<u>X</u> A return postcard.

<u>X</u> An Information Disclosure Statement (1 pg.), Form 1449 (2 pgs.), and copies of 34 cited references.

Communication Concerning Co-Pending Applications (2 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Ageounf No.

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Reg. No. 40,925

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(GENERAL)

PATENT

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Applicant:

Leonard Forbes

Examiner: Richard A. Booth

Serial No.:

09/945,512

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Filed:

August 30, 2001

Docket: 1303.027US1

Title:

IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL

BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING CO-PENDING APPLICATION(S)

Commissioner for Patents Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related entired patent application: pending application(s) in the above-identified patent application:

Serial No.	Filing Date	Attorney Docket	<u>Title</u>	MOM
08/903,453	07/29/1997	303.378US1	GATE INSULATOR FOR SILICO INTEGRATED CIRCUIT TECHN BY THE CARBURIZATION OF S	OLOGY
09/651,380	08/29/2000	N/A	THIN DIELECTRIC FILMS FOR STORAGE CAPACITORS	DRAM
09/943,134	08/30/2001	1303.020US1	PROGRAMMABLE ARRAY LOO MEMORY DEVICES WITH ASYMMETRICAL TUNNEL BAI	
09/945,137	08/30/2001	N/A	LOW COST PROCESSES FOR PRODUCING HIGH QUALITY PEROVSKITE DIELECTRIC FILE	MS
09/945,395	08/30/2001	1303.019US1	DRAM CELLS WITH REPRESSE MEMORY METAL OXIDE TUNI INSULATORS	
09/945,498	08/30/2001	1303.024US1	DYNAMIC ELECTRICALLY ALTERABLE PROGRAMMABLE MEMORY WITH INSULATING E OXIDE INTERPOLY INSULATO	METAL
09/945,500	08/30/2001	1303.029US1	PROGRAMMABLE MEMORY A AND DECODE DEVICES WITH TUNNEL BARRIER INTERPOLY	LOW

COMMUNICATION CONCERNING CO-PENDING APPLICATIONS

Serial Number: 09/945,512

Filing Date: August 30, 2001

IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

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Dkt: 1303.027US1

FLASH MEMORY WITH LOW TUNNEL 09/945,507 08/30/2001 1303.014US1 BARRIER INTERPOLY INSULATORS **GRADED COMPOSITION GATE** 09/945,514 08/30/2001 1303.015US1 INSULATORS TO REDUCE TUNNELING BARRIERS IN FLASH MEMORY **DEVICES** SRAM CELLS WITH REPRESSED 09/945,554 08/30/2001 1303.028US1 FLOATING GATE MEMORY, METAL OXIDE TUNNEL INTERPOLY **INSULATORS**

Respectfully submitted,

LEONARD FORBES

By Applicant's Representatives,

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